Scientific Staff Post- Junior Research Fellow

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Applications on the plain paper are invited for the post of Junior Research Fellow in the CSIR-DST Project, Electrical Engineering Department, Jamia Millia Islamia (A Central University), New Delhi on purely contractual basis with salary of Rs 20,000 (Twenty thousand) per month latest by 23.11.2014. It may revise to Rs 25,000+HRA of 30 % with approval of funding agency.

Initial appointment will be made for a period of 6 months with total salary of Rs 1,20,000 (One lac, twenty thousand Only). It is not obligatory on the part of the CSIR-DST project, Electrical Engineering Department, Jamia Millia Islamia (A Central University), New Delhi to call for interview every candidate who possesses the essential qualifications and no representation in this regard will be entertained from any candidate.

Educational Qualifications:

Good academic record with at least 65 % marks or an equivalent CGPA at the Graduate's degree level (B.Tech./ BSc. Eng./ B.E.) with GATE/ NET qualification or Post Graduate Degree level (M.Tech./ MSc. Eng./ M.E.) in the Electrical Electronics Engineering, Electronics and Communication Engineering or its related Engineering from an Indian University, or an equivalent degree from a foreign University.

The application complete in all respect should reach on or before **23.11.2014** addressed to Prof. M. Khan, Principal Investigator, CSIR-DST Project, Electrical Engineering Department, Faculty of Engineering & Technology, Jamia Millia Islamia (A Central University), New Delhi-110 025