ANAM KHAN

Mobile: +91-7889516096

Email: anamkhan06@gmail.com

Gender: Female

CAREER OBJECTIVE

Looking for an opportunity to work in a challenging environment and utilize my technical abilities and skills towards achieving the goals of the organization.

EDUCATIONAL QUALIFICATION

Qualification	Stream	Institute	Year of passing	CGPA or %	Grade
Ph.D	Electronics & Communication Engineering	Jamia Milia Islamia, New Delhi	2022		Awarded
M.Tech	Nanotechnology	Jamia Milia Islamia, New Delhi	2014	8.63	Distinction
B.Tech	Electronics & Communication	IUST, Awantipora, Kashmir, J&K	2011	7.73	Distinction
Sr. Sec (12th)	Non-medical & Functional English	Mallinson School. JK-BOSE	2006	89.33 %	Distinction (State topper)
Sec (10th)	All	Presentation Convent. JK- BOSE	2004	90%	Distinction (State topper)

OTHER QUALIFICATIONS

- Qualified GATE in 2012
- Qualified UGC-NET in 2015

LIST OF PUBLICATIONS

SCI Listed Journals

1. Anam Khan and Sajad A. Loan. “Metal Drain Double-Gate Tunnel Field Effect Transistor with Underlap: Design and Simulation.” *Silicon* (2020): 1-11.

2. Anam Khan et al. “Germanium Source Metal Drain Tunnel FET with Dual Dielectric Underlap.” *Silicon* 14 (2021): 1253-1262.

3. Anam Khan and Sajad A. Loan, “Germanium Pocket based Tunnel FET with underlap: Design and Simulation”. Analog Integrated Circuits and Signal Processing, 2022

4. Anam Khan and Sajad A. Loan, “Pocketed Dual Metal Gate TFET: Design and Simulation”, Materials Today Communications,Volume 35, 2023

5. Chadha, Dev, Anam Khan, Hend I. Alkhammash, and Sajad A. Loan. "Engineered Gate-Based Nanoscaled JK Flip-Flop: Design, Simulation and Applications." Nano (2024): 2450163.

6. M Salim Wani, Anam Khan, Hend I. Alkhammash and Sajad A. Loan “Dual-Metal-gate Germanium Pocket based Tunnel FET as a Label-Free Biosensor”, (Communicated,2024)

Publications in International Conferences

1. Anam Khan, S. A. Loan and A. G. Alharbi, "Germanium Source Double-Gate Tunnel Field Effect Transistor with Metal Drain: Design & Simulation," 2020 IEEE International Conference on Semiconductor Electronics (ICSE), 2020, pp. 21-24, doi: 10.1109/ICSE49846.2020.9166885.

2. Anam Khan and S. A. Loan, "Double Gate TFET with Germanium Pocket and Metal drain using Dual Oxide," 2021 International Conference on Microelectronics (ICM), 2021, pp. 170-173, doi: 10.1109/ICM52667.2021.9664949

3. Shaikh, M. Rizwan U., Mohd Shiblee, Anam Khan, and Sajad A. Loan. "Partial-Ground-Plane Junctionless Transistor on Selective Buried Oxide." In 2024 International Conference on Microelectronics (ICM), pp. 1-5. IEEE, 2024.

TRAININGS AND PROJECTS

- Eight days Training on NEP Orientation & Sensitization Programme from 3rd March 2025 to 12th March 2025
- M. Tech Project: 'Mathematical Modeling & Simulation of Carbon Nanotube/polymer nanocomposites'
- Two-month training at Tata Docomo CDMA in networking and communication
- B. Tech Project: 'Automated Car Parking' using sensor technology

LECTURES / SEMINARS ATTENTED

- International Workshop on Physics of Semiconductor Devices (IWPSD) in Amity University, Noida (2013)
- Interaction Program M.Tech ,Nanotechnology, Jamia Milia Islamia. 2012-2014
- 3rd International Mini Workshop on VLSI Design & Embedded Systems in Jawaharlal Nehru University (2019)
- Various Departmental Seminars Held Time to Time
- Three-day educational program in Regional Research Lab, Jammu. 2005

WORK EXPERIENCE

- Assistant Professor (Contractual) in **National Institute of Technology (NIT**), Srinagar

  From March 2015 to December 2017
- Guest Faculty in **Jamia Millia Islamia (JMI**), New Delhi

  From Aug 2023 till present.

KEY SKILLS

- Silvaco Atlas Simulation device Software
- RF Flow for Graphics Program implementation
- Origin software for Data Analysis & Scientific Graphing
- Knowledge of Integrated Circuits and VLSI design
- Excellent knowledge of Carbon Nanotubes, Nano devices and related Nano technological advancements
- Good knowledge of electric circuits, digital electronics and control system.
- Computer languages: C
- Software use: Matlab

- Assembly languages: Microprocessors 8085, 8086

PERSONAL DETAILS

Father's Name: Inayat-Ullah-Khan

Mother's Name: Shamshad Khan

Date of Birth: 6th of September,1988

Nationality: Indian

Marital Status: Married

Address: A-34/A, Third Floor. Lane 6. Abul Fazal Enclave. Jamia Nagar. Okhla. New Delhi. 110025